

	Type	L #	Hits	Search Text	DBs	Time Stamp
1	BRS	L1	510	simulat\$5 near4 inductor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/31 09:19
2	BRS	L2	0	arificial\$5 near4 inductor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/31 09:19
3	BRS	L3	15451	transistor\$5 near5 induct\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/31 09:20
4	BRS	L4	15901	1 or 3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/31 09:20
5	BRS	L5	717239	transistor\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/31 09:20
6	BRS	L6	558026	resistor\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/31 09:20
7	BRS	L7	3509	5 with 6 with 4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/31 09:20
8	BRS	L8	127933	voltage\$1 near4 higher	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/31 09:21

	Type	L #	Hits	Search Text	DBs	Time Stamp
9	BRS	L9	3	7 with 8	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/31 09:21
10	BRS	L10	651	7 and 8	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/03/31 09:21